

Robust fixed-time control for DC-DC converter with matched and mismatched perturbations

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This paper introduces a state-dependent variable exponent coefficient for fixed-time sliding mode control. The proposed state-dependent sliding surface ensures system stability and facilitates a rapid response within a fixed timeframe, while also addressing both matched and mismatched perturbations. This sliding mode control is applied to the DC-DC converter, a system that frequently encounters mismatched issues. By using the fixed-time concept, the designed sliding mode surface exhibits robustness, enabling it to manage both matched and mismatched perturbations within a fixed-time duration. We first present the average mathematical model for the continuous mode DC-DC converter. Next, a state-dependent sliding surface with a variable exponent coefficient is developed. Following next, the controller is designed to regulate the converter's voltage while effectively rejecting both matched and mismatched disturbances. The stability of the proposed approach is evaluated through the use of a Lyapunov function. Lastly, numerical simulations demonstrate rapid convergence and significant robustness against uncertainties in parameters, both matched and mismatched. When compared to other fixed-time and finite-time schemes, the proposed fixed-time controller is notable for its high efficiency and relatively straightforward control solution.

Keywords: DC-DC converter, fixed-time control, sliding mode control, state-dependent sliding surface, nonlinear control, mismatched parameter

1 Introduction

The DC-DC converter is widely utilized to stabilize the desired voltage output, which can be influenced by fluctuating conditions, nonlinearity, uncertainties, disturbances, and noise. The reliability of power during the conversion process is strongly dependent on the reliable operation of the power converter and its control mechanisms. A comprehensive review of the work in [1] provides essential insights into the classification of power converters and their corresponding control methods for various applications. The review indicates that numerous conventional converters and control strategies face various limitations in practical use, highlighting the necessity to thoroughly evaluate their effectiveness in specific systems. The buck converter is recognized as one of the simplest and most efficient power converters, capable of converting a higher DC voltage into a lower one. Its applications are varied, including micro-grids, renewable energy systems, photovoltaic configurations, battery charging, LED drivers, energy management, and speed control for both DC and AC motor drivers, among others. The proportional-integral-derivative (PID) control method is commonly utilized for DC-DC converters; however, it requires system linearization, which makes it more sensitive to external changes and uncertainties. Such issues present challenges for traditional linear regulators.

To address this challenge, a variety of non-linear control methods with a particular focus on sliding mode control (SMC) are employed. A detailed examination of

SMC, which emphasizes notable progress and investigates emerging trends and challenges in its application within power electronics, can be found in [2]. The concept of a fixed-time control system was later introduced in [3] and has since been further developed through numerous studies. In 2021, a robust variable exponent coefficient fixed-time SMC approach was presented [4]. This innovative variable exponent sliding mode strategy is simple to implement, non-singular, and robust against both matched and mismatched disturbances, while remaining independent of initial conditions. Owing to the difficulties in accurately determining disturbance values in practical applications, the gain of the sliding mode controller often exceeds the upper threshold of lumped disturbances to ensure robustness and reduce chattering problems. To overcome this problem, observer-based controllers have demonstrated effectiveness in reducing the effects of unexpected external disturbances, uncertainties in the model, and excessive chattering within control systems. An integrated sliding mode controller utilizing an early-aged extended state observer was described in [5]. The adaptive terminal SMC strategy, along with the continuous fast terminal SMC was introduced in [6-7]. In [8], a super-twisting sliding-mode observer technique was also proposed. A sliding mode observer was employed to estimate the lump disturbances of PMSM in [9].

There are a significant number of research studies focused on mismatch problems. Researchers have proposed various schemes, such as LMI-based SMC in [10]

and adaptive-based SMC in [11]. The modified SMC scheme requires the mismatch disturbance to satisfy the bounded assumption of the H_2 norm with mismatch disturbance as the non-vanishing uncertainty described by [12]. In recent years, DOB-based SMC approaches have been introduced to address mismatch disturbances. These methods relax the unreasonable H_2 norm-bounded assumption and maintain the stable performance. In [13], an adaptive fixed-time disturbance observer-based (DOB) controller was proposed to mitigate the mismatch disturbance. Subsequently, an extended state observer-based sliding mode control (SMC) method was introduced to address the mismatch disturbance observed in the buck converter application in [14]. The findings from the previous research were further developed into finite-time control in [15].

Previous research has addressed control issues involving both matched and mismatched disturbances by employing observers. Few fixed-time controllers have been proposed in academic research to solve the issue of mismatched disturbances in DC-DC buck converters. Therefore, this research is among the first to introduce a fixed-time controller utilizing a chosen sliding surface to suppress mismatched disturbances. This article introduces an approach known as variable exponent fixed-time sliding mode, based on [4], to reduce both types of disturbances without the use of observers. This work demonstrates that the DC-DC buck converter efficiently regulates DC voltage, successfully achieving the desired output voltage through the application of a fixed-time control strategy. The results indicate that fixed-time control theory provides a robust foundation for developing control laws. This proposed fixed-time approach is based on variable exponent coefficients.

This study is organized as follows. Section 2 provides an overview of the average dynamic model for a DC-DC converter. Section 3 details the theory of a fixed-time control and state-dependent sliding surface, as well as the proposition to solve mismatched parameters. Section 4 demonstrates a DC-DC converter with both matched and mismatched perturbations. In Section 5, the fixed-time controller is designed. Stability proof is also derived in Section 6. Section 7 presents the simulation results of the proposed controller, comparing its performance with other methods. Finally, Section 8 summarizes the findings related to the state-dependent controller.

2 Average state-space model of DC-DC converter

When a power supply needs to work with a wide range of voltages, such as in an adjustable switch-mode power supply, and the load is sufficiently high, the DC-DC converter can be characterized as operating in continuous conduction mode (CCM). Additionally, for the purposes of advanced control, the converter may also be represented in discontinuous conduction mode

(DCM) or composition mode. The conventional DC-DC converter depicted in Fig. 1 is widely utilized and has received significant attention in research studies. The converter is assumed to operate in DCM, as illustrated in Fig. 2, which can be classified into three separate regions. The fundamental operation of the buck converter can be described through the linear ordinary differential Eqns. (1), which are categorized as a non-smooth equation with a variable structure utilizing switching functions [16]:

$$\dot{v}_C = u_2 \left(\frac{i_L}{C} \right) - \frac{v_C}{RC}, \quad (1a)$$

$$\text{and} \quad \dot{i}_L = u_2 \left(\frac{u_1 v_{in} - v_C}{L} \right). \quad (1b)$$

The variable u_1 equals “1” when the switch is in the “on” position and equals “0” when the switch is “off”. The switching function of $u_2 = \text{sgn}[\text{sgn}(v_C) + \text{sgn}(i_L)]$ indicates that the status of the inductor determines the conduction mode of the converter, with v_C representing the voltage across the capacitor and i_L indicating the current flowing through the conductor. To limit the operational frequency, a common method is to utilize pulse width modulation (PWM), which is characterized by a constant switching period, T_s . The modulation duty cycle d serves as the control parameter in the state-space average dynamic model, representing the proportion of the period in which the switch remains in the “on” position. Furthermore, PWM effectively adjusts the output voltage, $v_C \in [0, v_{in}]$.

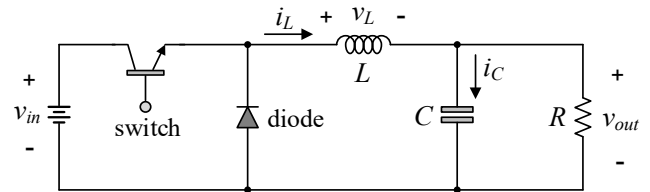


Fig. 1. Conventional DC-DC converter

According to Fig. 2, the operation regions of the DC-DC converter can be characterized by the following three state-space models:

1) CCM region I, when $u_2 = 1$ and $u_1 = 1$

$$A_1 = \begin{bmatrix} -\frac{1}{RC} & \frac{1}{C} \\ -\frac{1}{L} & 0 \end{bmatrix} \quad B_1 = \begin{bmatrix} 0 \\ \frac{1}{L} \end{bmatrix} \quad C_1 = [1 \quad 0] \quad D_1 = 0 \quad (2)$$

2) DCM region II, when $u_2 = 0$

$$A_2 = \begin{bmatrix} -\frac{1}{RC} & 0 \\ 0 & 0 \end{bmatrix} \quad B_2 = \begin{bmatrix} 0 \\ 0 \end{bmatrix} \quad C_2 = [1 \quad 0] \quad D_2 = 0 \quad (3)$$

3) CCM region III, when $u_2 = 1$ and $u_1 = 0$

$$A_3 = \begin{bmatrix} -\frac{1}{RC} & \frac{1}{C} \\ -\frac{1}{L} & 0 \end{bmatrix} \quad B_3 = \begin{bmatrix} 0 \\ 0 \end{bmatrix} \quad C_3 = [1 \quad 0] \quad D_3 = 0 \quad (4)$$

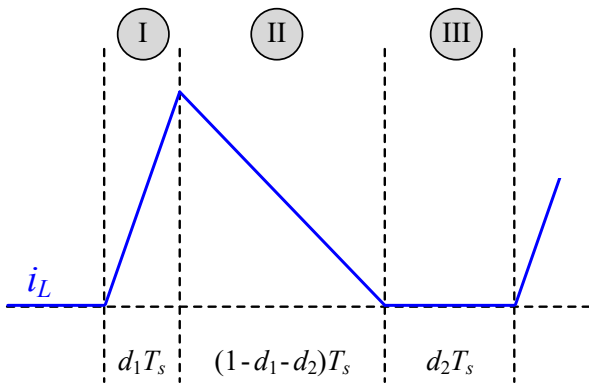


Fig. 2. Operation regions of the DC-DC converter

Using Eqn. (1), one can derive the average dynamic model of the CCM by:

$$\begin{aligned} A_{CCM} &= dA_1 + (1-d)A_3 \\ B_{CCM} &= dB_1 + (1-d)B_3 \\ C_{CCM} &= dC_1 + (1-d)C_3 \\ D_{CCM} &= dD_1 + (1-d)D_3 \end{aligned} \quad (5)$$

where d is the duty cycle of the pulse width modulation circuit.

Since the duty cycle modulation serves as an input for the state, this leads to

$$\begin{bmatrix} \dot{\bar{v}}_C \\ \dot{\bar{i}}_L \end{bmatrix} = \begin{bmatrix} -\frac{1}{RC} & \frac{1}{C} \\ -\frac{1}{L} & 0 \end{bmatrix} \begin{bmatrix} \bar{v}_C \\ \bar{i}_L \end{bmatrix} + \begin{bmatrix} 0 \\ \frac{v_{in}}{L} \end{bmatrix} d \quad (6)$$

Figure 3 shows the block diagram for the feedback control of a DC-DC converter. The controller compares the reference voltage v_r with the feedback voltage $\bar{v}_C(t)$ and uses this information to inform its actions. It

employs fixed-time stabilization and generates a duty cycle command d that regulates the gate in the PWM circuit, thereby controlling the output voltage $\bar{v}_C(t)$ of the DC-DC converter.

3 Robust fixed-time stability and state dependent sliding surface

The representation of the dynamic equation is as follows:

$$\dot{x}(t) = g[x(t)], \quad x(t) \in R^n, \text{ and } x(0) = x_0, \quad (7)$$

where $x = (x_1, x_2) \in R^2$ and g denotes a continuous function characterized by the equilibrium condition $g(0) = 0$.

Theorem 1: [3] A Lyapunov candidate function exists that is continuous, differentiable, positive definite, and radially unbounded $V: R^n \rightarrow R^+$, such that

$$\dot{V}(x) \leq -a_1 V(x)^\kappa - a_2 V(x)^{\phi_v}, \quad (8)$$

where $0 < \phi_v < 1 < \kappa$, $x \in R^n$, $a_1 > 0$, and $a_2 > 0$. The settling function (T) is represented as

$$T(x_0) \leq \frac{1}{a_1(1-\phi_v)} + \frac{1}{a_2(\kappa-1)}. \quad (9)$$

The stability of the robust variable exponent coefficient over a fixed time concept is described in the following:

Theorem 2: [17] The dynamical system defined by the state-dependent exponent coefficient can be characterized as

$$\dot{x} = -c|x(t)|^{\frac{\eta x(t)^2}{1+\nu x(t)^2}} \text{sgn}[x(t)] + \delta(t),$$

and

$$x(0) = x_0, \quad (10)$$

where $x(t) \in R$, $\eta > 0$, $\nu > 0$, and $\theta = \frac{\eta}{\nu+1} > 1$. In this case, the lumped disturbance $|\delta(t)|$ is positive bounded by constant ς . For the given $\varsigma > 0$ and $c > \varsigma e^{\frac{\eta}{2e}}$, the following describes the function T of the state-dependent system expressed in Eqn. (10).

$$T(x_0) \leq \frac{1}{(c-\varsigma)(\theta-1)} + \frac{1}{\left(c e^{\frac{\eta}{2e}} - \varsigma\right)}. \quad (11)$$

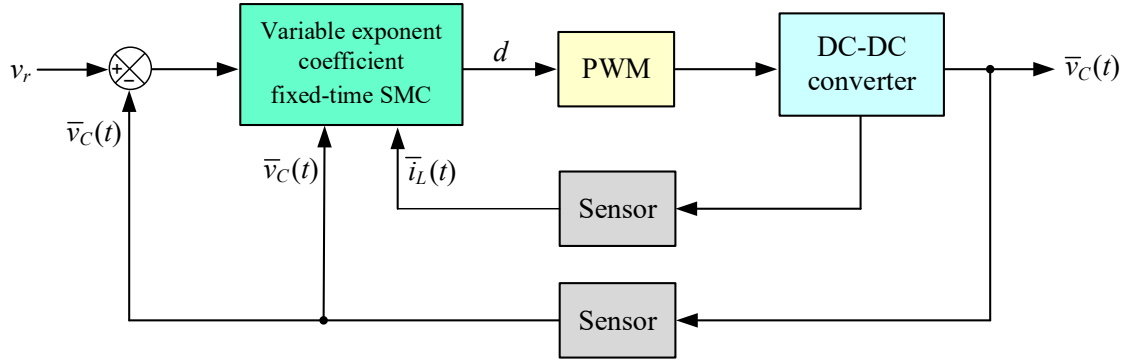


Fig. 3. Closed-loop control diagram of the DC-DC converter

Proof: Consider the Lyapunov function expressed below:

$$V(x) = x^2. \quad (12)$$

To achieve the desired outcome along the trajectory (10), the derivative of Eqn. (12) indicates that

$$\dot{V}(x) = -2c|x|^{\frac{\eta x^2}{1+\nu x^2}+1} + 2\zeta x. \quad (13)$$

Considering the first case $V(x) > 1$, we have $\left(\frac{\eta x^2}{1+\nu x^2} + 1\right) \geq \left(\frac{\eta}{1+\nu} + 1\right) > 2$. As $|x| \geq 1$ and $\theta = \frac{\eta}{1+\nu} > 1$, Eqn. (13) might be rewritten to

$$\begin{aligned} \dot{V}(x) &\leq -2(c - \zeta)|x|^{\theta+1} \\ &\leq -2(c - \zeta)V(x)^{\frac{\theta+1}{2}}. \end{aligned} \quad (14)$$

As for $(c - \zeta) > 0$ and $\frac{\theta+1}{2} > 1$, this ensures that the dynamical system described in Eqn. (10) with any initial condition $V(x) > 1$ reaches the sliding mode $V(x) \leq 1$ within a fixed period of time $T_1 \leq \frac{1}{(c-\zeta)(\theta-1)}$.

In the second case, where $V(x) \leq 1$, Eqn. (13) is rearranged as follows:

$$\dot{V}(x) = -2c|x|^{\frac{\eta x^2}{1+\nu x^2}} + 2\zeta x. \quad (15)$$

As $(1+\nu x^2) \geq 1$ and $|x| \leq 1$, the state dependent function is simplified to $\min\left(|x|^{\frac{\eta x^2}{1+\nu x^2}}\right) \geq \min\left(|x|^{\eta x^2}\right) = e^{-\frac{\eta}{2e}}$ and we can rewrite Eqn. (13) as

$$\begin{aligned} \dot{V}(x) &\leq -2(ce^{\frac{-\eta}{2e}} - \zeta)|x| \\ &\leq -2(ce^{\frac{-\eta}{2e}} - \zeta)V(x)^{\frac{1}{2}}, \end{aligned} \quad (16)$$

where $\left(ce^{\frac{-\eta}{2e}} - \zeta\right) > 0$. It means that all the solutions are starting from $V(x) \leq 1$ and reach the equilibrium in a time period of $T_2 \leq \frac{1}{\left(ce^{\frac{-\eta}{2e}} - \zeta\right)}$. Finally, the dynamical system (10) achieves for the equilibrium in a fixed time $T(x_0) \leq (T_1 + T_2)$.

Proposition 1: In order to implement voltage regulation in the presence of both matched and mismatched perturbations, the control input can be assigned in the following manner:

$$u = u_{eq} + u_b, \quad (17)$$

where

$$u_{eq} = \varphi(z)|x_1| \left[x_2 + k_2 \operatorname{sgn}(s) \right] + \left(\frac{x_1}{LC} \right) + \left(\frac{x_2}{R_o C} \right) + \delta, \quad (18)$$

$$u_b = -k_1 |s|^{\frac{\eta_2 s^2}{1+\nu_2 s^2}} \operatorname{sgn}(s), \quad (19)$$

$$\varphi(z) = \left(\frac{\beta \eta_1}{1+\nu_1 z^2} \right) \left[\frac{2x \ln(z)}{1+\nu_1 z^2} + 1 \right] z^{\frac{\eta_1 z^2}{1+\nu_1 z^2} + 1}, \quad (20)$$

and $k_1 > 0$, $k_2 > 0$, $\eta_1 > 0$, $\nu_1 > 0$, and $\theta_2 = \frac{\eta_2}{1+\nu_2} > 1$.

The closed-loop controller outlined in Eqns. (17-20) is designed to ensure that the system reaches the equilibrium region stated in Eqn. (11)

$$T(x_0) \leq \frac{1}{(k_1 - \zeta_3)(\theta_2 - 1)} + \frac{1}{\left(k_1 e^{\frac{-\eta_2}{2e}} - \zeta_3\right)} + \frac{1}{(\beta - \zeta_1)(\theta_1 - 1)} + \frac{1}{\left(\beta e^{\frac{-\eta_1}{2e}} - \zeta_1\right)}$$

4 Matched and mismatched perturbations of nonlinear systems

This section discusses the development of a fixed-time sliding surface that guides the system toward the equilibrium trajectory, despite the occurrence of matched and mismatched disturbances.

Lemma 1: [4] This involves the analysis of a nonlinear second-order system that exhibits both matched and mismatched characteristics:

$$\begin{aligned}\dot{x}_1 &= x_2 + \sigma_1 \\ \dot{x}_2 &= f(x) + g(x)u + \sigma_2\end{aligned}\quad (21)$$

In the above relation, $x = (x_1, x_2) \in R^2$ and $u \in R$ are defined as the control inputs. Functions f and g are continuous functions with $f(0) \neq 0$ and $g(x) \neq 0$ for all $x \in R^2$. The term σ_1 represents a mismatched disturbance constrained by $|\sigma_1(t)| < \varsigma_1$, while σ_2 denotes a matched disturbance with $|\sigma_2(t)| < \varsigma_2$. In the absence of a controller capable of managing mismatched parameters, the neighbourhood equilibrium set of system (21) is approximately $[0, -\sigma_1(t)]$. The sliding variable associated with the state-dependent variable exponent coefficient can be defined as follows:

$$s(x) = x_2 + \beta |x_1|^{\frac{\eta_1 x_1^2}{1+v_1 x_1^2}} \text{sgn}(x_1), \quad (22)$$

where $x \in R$, $\eta_1 > 0$, $v_1 > 0$, $\theta = \frac{\eta_1}{1+v_1} > 1$ and $\beta > \varsigma_1 e^{\frac{\eta_1}{2\theta}}$.

The closed-loop dynamical system is a robust, globally fixed-time stable system characterized by a steady state period within

$$\begin{aligned}T(x_0) \leq & \frac{1}{(k_1 - \varsigma_3)(\theta_2 - 1)} + \frac{1}{k_1 e^{\frac{-\eta_1}{2\theta}} - \varsigma_3} \\ & + \frac{1}{(\beta - \varsigma_1)(\theta_1 - 1)} + \frac{1}{\beta e^{\frac{-\eta_1}{2\theta}} - \varsigma_1}\end{aligned}\quad (23)$$

where $k_1 > \varsigma_3 = \varsigma_2 + 2\beta\eta_1(1+v_1)(\varsigma_1 + k_2)$ and $k_2 > \varsigma_1$.

Lemma 2: [4] For $Z \geq 0$, we have $\varphi(z) > 0$ when $z > 1$, and $|\varphi(z)| \leq 2\beta\eta_1(1+v_1)$ when $0 \leq z \leq 1$.

Proof: According to proposition 2 in [4], when $0 \leq z \leq 1$, we have

$$\varphi(z) = \frac{\beta\eta_1}{(1+v_1 z^2)^2} [2z \ln(z) + z + v_1 z^3] z^{\frac{\eta_1 z^2}{1+v_1 z^2}}. \quad (24)$$

Note that $0 \leq \frac{\beta\eta_1}{(1+v_1 z^2)^2} \leq \beta\eta_1$ and $0 \leq z^{\frac{\eta_1 z^2}{1+v_1 z^2}} \leq 1$.

From Eqn. (24), we establish the following relation for clarity:

$$\psi(z) = 2z \ln(z) + z + v_1 z^3. \quad (25)$$

Differentiating (25) and setting it to 0 results in

$$\psi'(z) = 2 \ln(z) + 3 + 3v_1 z^2 = 0. \quad (26)$$

We observe that ψ decreases on the interval $(0, z_0)$ and increases on the interval $(z_0, 1)$. This leads to $|\psi(z)| \leq \max\{1+v_1, |\psi(z_0)|\}$, and the result can be then calculated as

$$\psi(z_0) = -2z_0(1+v_1 z_0). \quad (27)$$

Since $0 \leq z \leq 1$, the final result is obtained as $|\psi(z_0)| \leq 2(1+v_1)$.

5 Controller design

Even when there are matched and unmatched disturbances, uncertainties, unmodeled characteristics, and noise, the state-dependent controller is designed to consistently track the reference signal. This work focuses on regulating the output voltage of a DC-DC converter. Consequently, Eqn. (6) allows for the following derivation.

Suppose that $x_1 = e = \bar{v}_C - v_r$, we have

$$\dot{x}_1 = \frac{\bar{i}_L}{C} - \frac{\bar{v}_C}{R_o C} + \sigma_1(t), \quad (28)$$

where $\sigma_1(t) = \frac{\bar{v}_C}{R_o C} - \frac{\bar{v}_C}{RC}$ is assigned as the mismatched parameters.

Also let us define that $x_2 = \frac{\bar{i}_L}{C} - \frac{\bar{v}_C}{R_o C}$, then we have

$$\dot{x}_2 = \frac{(dv_{in} - v_r)}{LC} - \frac{x_1}{LC} - \frac{x_2}{R_o C} - \sigma_2(t), \quad (29)$$

where $\sigma_2(t) = \left(1 - \frac{R_o}{R}\right) \left(\frac{\bar{v}_C}{R_o C}\right)$ is assigned as the mismatched parameters.

Based on Eqns. (28) and (29), the dynamic model of the DC-DC converter under mismatched perturbations can be expressed as:

$$\begin{aligned}\dot{x}_1 &= x_2 + \sigma_1(t) \\ \dot{x}_2 &= u - \frac{x_1}{LC} - \frac{x_2}{R_o C} - \sigma_2(t),\end{aligned}\quad (30)$$

where $u = \frac{(dv_{in} - v_r)}{LC}$ is lumped as the state input.

6 Fixed-time stability proof

The block diagram of the proposed sliding surface with reaching law is shown in Fig. 4. By employing the sliding surface delineated in Eqn. (22), we can assert the following:

$$s = x_2 + \beta |x_1|^{\frac{\eta_1 x_1^2}{1+v_1 x_1^2}} \text{sgn}(x_1). \quad (31)$$

The derivative of Eqn. (31) yields

$$\dot{s} = u - \frac{x_1}{LC} - \frac{x_2}{R_o C} - \sigma_2(t) + \frac{\beta \eta_1 |x_1| (x_2 + \sigma_1)}{1 + v_1 x_1^2} \left(\frac{2 \ln |x_1|}{1 + v_1 x_1^2} + 1 \right) |x_1|^{\frac{\eta_1 x_1^2}{1+v_1 x_1^2}}. \quad (32)$$

Referring to *Proposition 1*, we will substitute the control signal u from Eqns. (17-19) into (32) and simplify the resulting expression:

$$\dot{s} = -k_1 |s|^{\frac{\eta_2 s^2}{1+v_2 s^2}} \text{sgn}(s) + \delta - \sigma_2(t) + \varphi(z) |x_1| [\sigma_1 - k_2 \text{sgn}(s)] \quad (33)$$

The derivative of the sliding surface is, in fact,

$$\dot{s} = -u_b + \delta - \sigma_2(t) - \varphi(z, \sigma_1, k_2). \quad (34)$$

The Lyapunov candidate function is defined as

$$V = s^2. \quad (35)$$

The derivative of this function with trajectory (33) reveals two different cases. It is evidently clear that

$$\dot{V}(s) \leq 2|s| \left\{ -k_1 |s|^{\frac{\eta_2 s^2}{1+v_2 s^2}} + \delta - \sigma_2(t) + \varphi(z) |x_1| [\text{sgn}(s) \sigma_1 - k_2] \right\}. \quad (36)$$

For the first case, when $|x_1| > 1$, we find that $\varphi(z)|x_1| > 0$. Therefore,

$$\dot{V}(s) \leq 2|s| \left[-k_1 |s|^{\frac{\eta_2 s^2}{1+v_2 s^2}} + \varsigma_2 + \varphi(z) |x_1| (\varsigma_1 - k_2) \right]. \quad (37)$$

By assumption in *Lemma 2*, we know that $k_2 > \varsigma_1$, then we have $\varphi(z) |x_1| (\varsigma_1 - k_2) < 0$. We can rewrite Eqn. (37) as a simple expression as:

$$\dot{V}(s) \leq 2|s| \left(-k_1 |s|^{\frac{\eta_2 s^2}{1+v_2 s^2}} + \varsigma_2 \right). \quad (38)$$

For the second case, when $|x_1| \leq 1$, and referring to *Lemma 2*, we have $|\varphi(z) |x_1|| \leq 2\beta\eta_1(1 + v_1)$. Then Eqn. (37) can be written as:

$$\dot{V}(s) \leq 2|s| \left(-k_1 |s|^{\frac{\eta_2 s^2}{1+v_2 s^2}} + \varsigma_3 \right), \quad (39)$$

$$\text{where } \varsigma_3 = \varsigma_2 + 2\beta\eta_1(1 + v_1)(\varsigma_1 + k_2). \quad (40)$$

Application of *Theorem 1* demonstrates that the closed-loop dynamical system described in (33), starting at initial surface $s(0) = s_0$, will achieve $s = 0$ within a fixed time satisfying

$$T(s_0) \leq \frac{1}{(k_1 - \varsigma_3)(\theta_2 - 1)} + \frac{1}{\left(k_1 e^{\frac{-\eta_2}{2e}} - \varsigma_3\right)}.$$

According to Eqn. (31), when the sliding surface $s = 0$, it follows that

$$\dot{x}_1 = -\beta |x_1|^{\frac{\eta_1 x_1^2}{1+v_1 x_1^2}} \text{sgn}(x_1) + \sigma_1. \quad (41)$$

We observe that $x_1(t)$ starts at $x_1(0) = x_{10}$ and reaches equilibrium within a fixed time satisfying

$$T(x_{10}) \leq \frac{1}{(\beta - \varsigma_1)(\theta_1 - 1)} + \frac{1}{\left(\beta e^{\frac{-\eta_1}{2e}} - \varsigma_1\right)}.$$

With *Theorem 2*, we determine that $x_2(t)$ tracks $-\sigma_1(t)$ over a fixed-time period. Accordingly, we obtain a fixed-time estimator for the mismatched disturbance without utilizing observers. By substituting the control signals from Eqns. (17-19) into (32), the dynamical system reaches the equilibrium region in a fixed time satisfying the condition $T(x_0) \leq T(s_0) + T(x_{10})$.

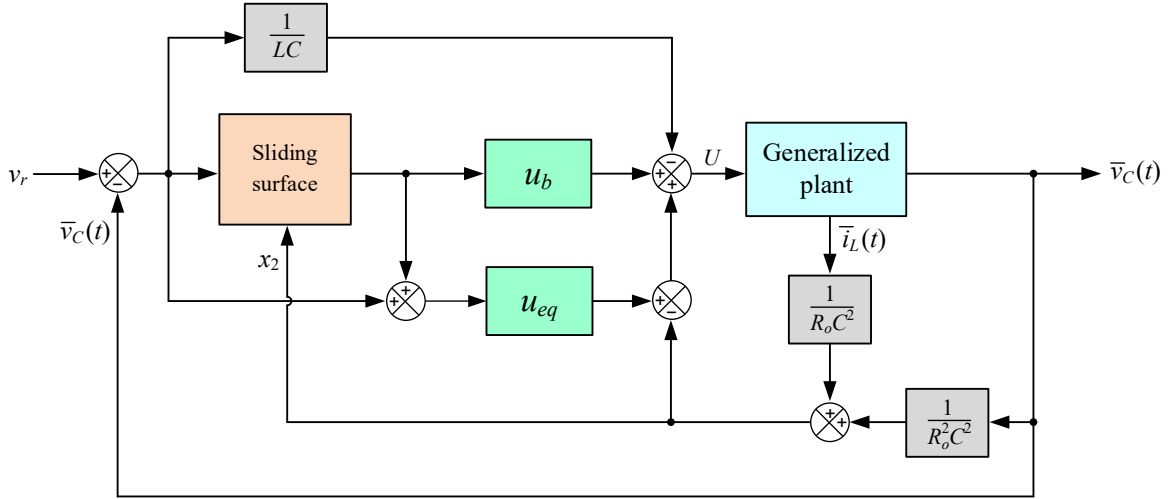


Fig. 4. Block diagram of the proposed sliding mode control system

7 Simulation results

The system depicted in Fig. 1 has been simulated using both a conventional proportional-integral (PI) controller and the proposed fixed-time SMC in MATABL. The buck converter parameters were set as follows: $v_{in} = 30$ V, $R = 6.71$ Ω , $L = 40$ mH, and $C = 4$ μ F. Step references (v_r) of 10 V, 25 V, and 15 V were used to evaluate tracking performance under both matched and mismatched perturbations.

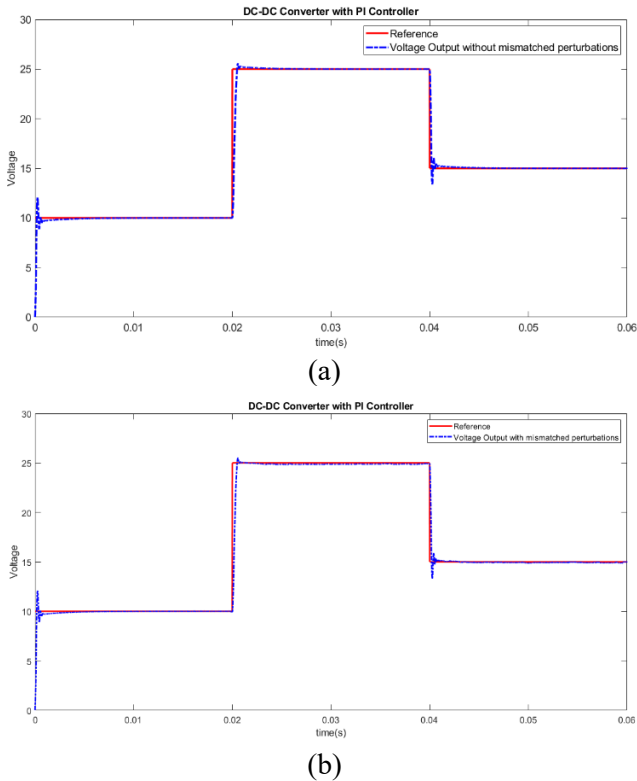


Fig. 5. Output voltage of the DC-DC converter using PI controller: (a) without mismatched perturbations, (b) with mismatched perturbations

Figure 5 shows the simulated output voltage of the system controlled by a PI controller with parameters set at $K_p = 200$ and $K_i = 0.5$. As illustrated in Fig. 5(a), the PI controller demonstrates generally effective performance; however, it is noted that overshoot occurs during instances of speed variation. Ultimately, the PI controller is capable of driving the system toward the setpoint without any steady-state error. Nonetheless, when parameters are mismatched, the PI controller can still function in this situation. However, there will be some voltage error during speed changes and the steady-state period. As can be observed from Fig. 5(b), the PI controller needs a few second to recover the system to a state of zero error.

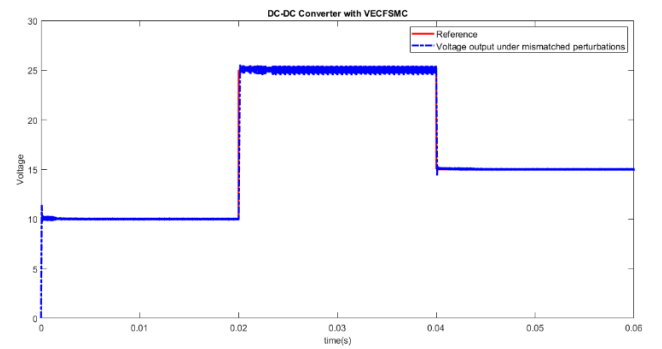


Fig. 6. Output voltage of the DC-DC converter using the proposed fixed-time SMC under mismatched perturbations

Figure 6 shows the output voltage of the DC-DC converter using the proposed fixed-time SMC in Fig. 4. The perturbation terms are defined as:

$$\sigma_1(t) = 0.1 - 0.2 \sin(0.4t),$$

$$\sigma_2(t) = 0.2 \sin(t) - 0.4 \sin(3t).$$

The configuration of the controller parameters follows the guidelines outlined in [4]. As a result, the selected parameters are recommended to satisfy the conditions:

$$\varsigma_1 \geq \sigma_1, \varsigma_2 \geq \sigma_2, \text{ and } k_1, k_2 > \varsigma_3.$$

To ensure that

$$\theta = \frac{\eta_1}{1+v_1} > 1 \text{ and } \beta > \varsigma_1 e^{\frac{\eta_1}{2e}},$$

the parameters η_1 and v_1 must be greater than zero ($\eta_1 > 0$ and $v_1 > 0$). The specific parameters are defined as follows: $k_1 = k_2 = 2$, $\eta_1 = 1.2$, $v_1 = 1$, $\eta_2 = 4$, $v_2 = 1$, $\varsigma_1 = 0.3$, $\varsigma_2 = 0.6$, and $\beta = 5.5$. The system demonstrates exceptional performance in tracking voltage commands without any overshoot. There is no steady-state error during the voltage change or the steady-state period. The response is faster than that of a PI controller. However, the output voltage exhibits some chattering due to the high switching function. The system is not only stable and resilient but also consistently provides power to the load.

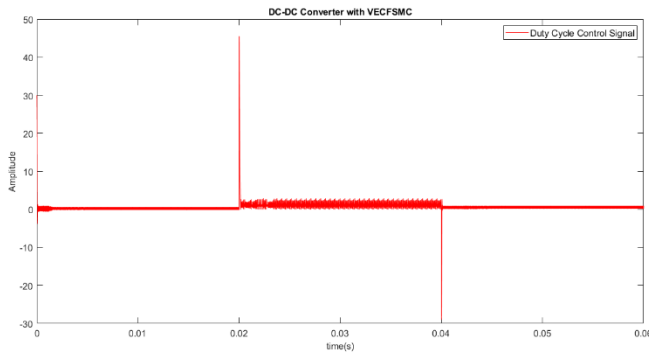


Fig. 7. Duty cycle control signal of the proposed fixed-time SMC

Figure 7 illustrates the corresponding duty cycle signal generated by the controller. While high-frequency switching allows for accurate tracking, it also introduces some chattering, which is a common issue in SMC. This minor limitation can be addressed in future work by exploring continuous sliding mode approaches or boundary layer techniques to minimize switching activity.

In summary, the proposed fixed-time SMC clearly outperforms the PI controller, particularly in speed, robustness, and accuracy. Its simple structure, observer-free design, and strong disturbance rejection capabilities

make it an effective and practical solution for voltage regulation in DC-DC converters operating under uncertain conditions.

8 Conclusions

This paper presents a variable exponent coefficient sliding surface designed to control the output voltage of a DC-DC converter, addressing both matched and mismatched disturbances in the mathematical model. It considers the dynamic model of the converter, incorporating uncertainties and lumped load disturbances. The conventional PI controller was employed to regulate the output voltage and exhibited excellent performance in the absence of mismatched disturbances. Nevertheless, it demonstrates certain distortion in the voltage output when confronted with mismatched parameters. The fixed-time sliding mode control method, utilizing a variable exponent coefficient and functioning without an observer, efficiently regulates voltage and mitigates both matched and mismatched disturbances. A stability proof is presented for the fixed-time sliding controller. The proposed variable exponent coefficient sliding mode controller offers a simple solution for voltage regulation, requiring only a few parameters. This fixed-time variable exponent coefficient approach is more straightforward compared to other finite or fixed-time methods, making it suitable for real-world applications. The proposed controller shows a strong response without requiring an observer; however, it exhibits chattering when compared to the linear control method. Future work may explore more sophisticated sliding surfaces and reaching laws to reduce chattering. Additionally, it is important to note that as mathematical complexity increases, the challenges associated with implementation also increase.

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